Avoiding Synchronization in First-Order Methods for Sparse Convex Optimization

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Abstract—Parallel computing has played an important role in speeding up convex optimization methods for big data analytics and large-scale machine learning (ML). However, the scalability of these optimization methods is inhibited by the cost of communicating and synchronizing processors in a parallel setting. Iterative ML methods are particularly sensitive to communication cost since they often require communication every iteration. In this work, we extend well-known techniques from Communication-Avoiding Krylov subspace methods to first-order, block coordinate descent methods for Support Vector Machines and Proximal Least-Squares problems. Our Synchronization-Avoiding (SA) variants reduce the latency cost by a tunable factor of s at the expense of a factor of s increase in flops and bandwidth costs. We show that the SA-variants are numerically stable and can attain large speedups of up to $5.1 \times$ on a Cray XC30 supercomputer.

Keywords-Synchronization-Avoiding; Support Vector Machines; Proximal Least-Squares; Sparse Convex Optimization; Coordinate Descent Methods;

I. INTRODUCTION

The running time of algorithms on a distributed-memory, parallel machine depends on the number of arithmetic operations F (computation) and the cost of data movement (communication). In distributed-memory settings, communication cost includes the "message size cost," i.e., the amount, W, of data exchanged between processors over a network (bandwidth cost) and the "synchronization cost," i.e., the number, L, of messages sent, where a message is used for interprocessor synchronization (latency cost). On modern parallel computer architectures, communicating data often takes much longer than performing a floating-point operation, and this gap is continuing to increase. Therefore, it is important to design algorithms that minimize communication in order to attain high performance, and recent results on communication-avoiding numerical linear algebra (CA-NLA) [1] illustrate the potential for faster algorithms by avoiding interprocessor communication.

Our goal in this work is to extend results from CA-NLA to convex optimization [2] to accelerate machine learning (ML)

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applications. In particular, we are interested in reducing communication cost for iterative convex optimization on distributed machines. Communication is a bottleneck for these methods since they often require synchronization at every iteration. Our goal in this paper is to avoid this communication for s iterations, where s is a tuning parameter, without altering the convergence rates or numerical stability of the existing methods. We will show how the communication-avoiding technique developed for Krylov subspace methods [1, Sec. 8] can be used to derive faster convex optimization algorithms.

We are interested in solving the class of optimization problems which take the form

$$\operatorname*{arg\,min}_{x \in \mathbb{R}^n} f(A, b, x) + g(x). \tag{1}$$

where f(A, b, x) is a convex loss function with regularization function, g(x), $A \in \mathbb{R}^{m \times n}$ with m data points and n features, labels $b \in \mathbb{R}^m$, and the solution to the minimization problem, $x \in \mathbb{R}^n$. In particular, we would like to focus on sparse convex optimization problems: sparse proximal least-squares [3], $(f(A, b, x) = \frac{1}{2n} ||Ax - b||_2^2)$ with sparsity-inducing regularization functions

Lasso [4]:	$g(x) = \lambda x _1,$
Elastic-Nets [5]:	$g(x) = \lambda x _2^2 + (1 - \lambda) x _1,$
Group Lasso [6]:	$g(x) = \lambda \sum_{g=1}^{G} \tilde{x}_g _2,$

where $\{\tilde{x}_1, \tilde{x}_2, \dots, \tilde{x}_G\}$ are *G* disjoint blocks of *x*. We also consider support vector machines (SVMs) [7]

$$f(A, b, x) = \lambda \sum_{i=1}^{m} \max(1 - b_i A_i x, 0)^2,$$

where A_i is the *i*-th row of A, b_i is the corresponding binary label ({+1, -1}), and λ is a regularization parameter. We present our results for proximal least-squares using Lassoregularization, but they hold more generally for other regularization functions with well-defined proximal operators (Elastic-Nets, Group Lasso, etc.) [3]. By proximal operator



Figure 1: A high-level depiction of the Block Coordinate Descent method (independent of the minimization problem being solved). The matrix A is 1D-row partitioned and w.l.o.g. depicted as being dense. Vectors in the partitioned dimension are also partitioned (in this case residual vector, r). Vectors in the non-partitioned dimension and all scalars are replicated (in this case x). Each processor selects the same column indices (by using the same random generator seed). Computation of (partial) dot-products is a local GEMM operation. After that, the results are combined using an Allreduce with summation. Due to data replication all processors can independently compute this iteration's solution and perform vector updates.

we mean that the non-linearity due to the regularization function, for example with Lasso, can be defined for a constant, α , and element-wise on vector, β , as:

$$S_{\alpha}(\beta_i) := sign(\beta_i) \max(|\beta_i| - \alpha, 0), \qquad (2)$$

where $S_{\alpha}(\beta)$ is the well-know soft-thresholding operator for Lasso [4], [8], [9]. Proximal operators, similar to (2), can be defined for other non-linear regularizers (i.e. Elastic-Nets, Group Lasso, etc.). Lasso creates solution sparsity during the optimization process by setting elements of the solution vector, x, exactly to zero. SVM [7] introduces sparsity (by its loss function definition) since we seek a small number of support vectors which separate data belonging to different classes. The sparsity-inducing nature of both optimization problems is important (and widely used) when dealing with high-dimensional data. In the case of high-dimensional and large-scale data, it becomes equally important to parallelize the computations in order to quickly and efficiently solve these Lasso and SVM problems.

There exist many algorithms to solve proximal leastsquares [10]–[14] and SVM problems [15]–[19]. In this paper we focus on randomized variants of accelerated and nonaccelerated Coordinate Descent (CD) and Block Coordinate Descent (BCD) since they have optimal convergence rates among the class of first-order methods [10]–[13], [16], [19].

CD [20] is a popular ML technique for solving optimization problems which updates a single element (i.e., coordinate) of x by minimizing a one-dimensional subproblem. BCD generalizes CD with a tunable block size parameter, μ , by updating μ coordinates of x and minimizing a μ dimensional subproblem. This process is repeated until a termination criterion is met. Solving the subproblem initially requires the computation of several dot-products with a subset of rows or columns of the data matrix, A, its transpose, and residual vectors. Once they are computed the remaining computations are scalar and vector operations which update the solution and residual vectors (and scalar quantities) for the next iteration.

Figure 1 illustrates the computations in parallel BCD. We assume that A is partitioned so that the dot-products can be computed in parallel by all processors (in this case rowpartitioned). Vectors in \mathbb{R}^m are similarly partitioned (in this case residual, r), but vectors in \mathbb{R}^n are replicated (in this case solution, x). For $\mu > 1$ the dot-products become GEMM (GEneralized Matrix-Multiplication) computations. After the dot-product/GEMM computations an MPI_Allreduce with summation combines each processor's contributions. Since Allreduce redundantly stores results on all processors, computing the solution to the subproblem and updating all vectors can be performed without communication. Finally, this process is repeated until a termination criterion is met. The mathematical details (and complexity) of solving the subproblem and updating vectors might vary based on the optimization problem (proximal least-squares, SVM, etc.). However, the parallel BCD methods we consider in this paper can be summarized by Figure 1. Each iteration requires synchronization, therefore, avoiding these synchronization costs could lead to faster and more scalable BCD methods for proximal least-squares and SVM.

Main Contributions: We present synchronizationavoiding (SA) derivations of existing accelerated and nonaccelerated CD/BCD through mathematical re-formulation. We call these methods synchronization-avoiding since they trade-off additional bandwidth and flops for fewer synchronizations. We show that our methods avoid synchronization for *s* iterations *without* altering the numerical stability or convergence behavior in exact arithmetic. We implement the SA-methods in C++ using MPI [21] and show that our methods perform $1.2 \times -5.1 \times$ faster and are more scalable on up to 12k cores of a Cray XC30 supercomputer on LIBSVM [22] datasets.

The rest of the paper is organized as follows: Section II discusses related approaches and how ours differs. Section IV derives SA-Lasso and shows experimental results in Section IV. Section IV derives SA-SVM and shows experimental results in Section V-B. Finally, we summarize and discuss future work in Section VI.

II. RELATED WORK

Communication-Avoiding in Linear Algebra: This paper extends the *s*-step iterative methods work in NLA and subsequent work on CA iterative linear algebra surveyed by Ballard et. al. [1]. We extend these results to widely used and important problems in ML, namely proximal least-squares and SVM. Our work expands upon and generalizes CA-NLA results to the field of convex optimization.

Communication-Efficient Machine Learning: Recent work along these lines, like proxCoCoA+ [23] propose frameworks that reduce the communication bottleneck. Prox-CoCoA+ perform computation on only locally stored data and then combine contributions from all processors. The communication benefits of proxCoCoA+ are inherited from the iteration complexity of performing Newton-type steps on locally stored data.

DUAL-LOCO [24] introduces a framework which reduces communication between processors by communicating a small matrix of randomly projected features. While DUAL-LOCO requires just one communication round, it does not apply to proximal least-squares problems (i.e., Lasso, elasticnet, etc.) and introduces an (additive) approximation error.

CA-SVM [25] eliminates communication in SVM by performing an initial K-means clustering as a pre-processing step to partition the data and subsequently training SVM classifiers locally on each processor. Communication is reduced significantly, but at the cost of accuracy. Like proxCoCoA+, CA-SVM uses a local SVM solver which can be replaced with our SA-variant.

P-packSVM [26] applies a similar approach to ours and derives a SA version of SVM using Stochastic Gradient Descent (SGD) as the optimization method. Our work extends this SA approach further to different methods (accelerated and non-accelerated CD/BCD) and extend it to other non-linear optimization problems (proximal least-squares).

Devarakonda et al. [27] derive SA-variants of primal and dual BCD methods for L2 regularized least-squares. Our Algorithm 1 Accelerated Block Coordinate Descent (accBCD) for Lasso

1: **Input**: $A \in \mathbb{R}^{m \times n}, b \in \mathbb{R}^m, H > 1, y_0 \in \mathbb{R}^n, z_0 \in \mathbb{R}^n$, $\lambda \in \mathbb{R}, \ \mu \in \mathbb{Z}_+ \ \text{s.t.} \ \mu \leq n$ 2: $\theta_0 = \mu/n, \ \tilde{y}_0 = Ay_0, \ \tilde{z}_0 = Az_0 - b$ 3: $q = \lceil n/\mu \rceil$ 4: for $h = 1, 2, \cdots, H$ do choose $\{i_l \in [n] | l = 1, 2, \dots, \mu\}$ uniformly at 5: random without replacement. 6: $\mathbb{I}_h = \left| e_{i_1}, e_{i_2}, \cdots, e_{i_u} \right|$ Let $\mathbb{A}_h = A\mathbb{I}_h$ 7: Communication: Lines 8 and 9. $G = \mathbb{A}_h^T \mathbb{A}_h$ 8: $r_{h} = \mathbb{A}_{h}^{T} \left(\theta_{h-1}^{2} \tilde{y}_{h-1} + \tilde{z}_{h-1} \right)$ v = largest eigenvalue of G9: 10: $\eta_h = \frac{1}{q\theta_{h-1}e_h^T v}$ $g_h = \mathbb{I}_{i_h}^T z_{h-1} - \eta_h r_h$ $\Delta z_h = S_{\lambda\eta_h}(g_h) - \mathbb{I}_h^T z_{h-1}$ 11: 12: 13: $z_h = z_{h-1} + \mathbb{I}_h \Delta z_h$ 14: $\begin{aligned} & \tilde{z}_{h} = \tilde{z}_{h-1} + \mathbb{I}_{h} \Delta z_{h} \\ & \tilde{z}_{h} = \tilde{z}_{h-1} + \mathbb{A}_{h} \Delta z_{h} \\ & y_{h} = y_{h-1} - \frac{1 - q\theta_{h-1}}{\theta_{h-1}^{2}} \mathbb{I}_{h} \Delta z_{h} \\ & \tilde{y}_{h} = \tilde{y}_{h-1} - \frac{1 - q\theta_{h-1}}{\theta_{h-1}^{2}} \mathbb{A}_{h} \Delta z_{h} \\ & \theta_{h} = \frac{\sqrt{\theta_{h-1}^{4} + 4\theta_{h-1}^{2} - \theta_{h-1}^{2}}}{2} \end{aligned}$ 15: 16: 17: 18: 19: **Output** $\theta_H^2 y_H + z_H$

work extends their results to non-linear problems and to sparse convex optimization.

Soori et al. [28] derive CA-variants of novel stochastic FISTA (SFISTA) and stochastic Newton (SPNM) methods for the proximal least squares problem. They illustrate that standard loop unrolling techniques can be applied to SFISTA and SPNM to obtain communication-avoiding variants. However, it should be noted that SFISTA and SPNM have communication costs which scale quadratically with the dimensions of the input matrix. On the other hand, BCD methods use a tunable blocksize parameter which has smaller bandwidth and flops requirements. Furthermore, our work solves the proximal least-squares and SVM problems on accelerated and non-accelerated BCD methods. Furthermore, the synchronization-avoiding technique is adapted from CA-Krylov [1, Sec. 8] and *s*-step Krylov subspace methods [29]–[32] work.

Our SA technique derives alternate forms of existing proximal least-squares and SVM methods by re-arranging the computations to obtain *s* solution updates per communication round. This allows us to obtain an algorithm whose convergence behavior and sequence of solution updates are equivalent to the original algorithm.

III. SYNCHRONIZATION-AVOIDING LASSO

In this section, we derive a SA version of the accelerated block coordinate descent (accBCD) algorithm for the Lasso problem. The derivation of SA-accBCD (Synchronization-Avoiding accBCD) relies on unrolling the vector update recurrences s times and re-arranging the updates and dependent computations to avoid synchronization.

Given a matrix $A \in \mathbb{R}^{m \times n}$ with m data points and n features, a vector of labels $b \in \mathbb{R}^m$, and regularization parameter $\lambda \in \mathbb{R}$, the Lasso problem aims to find the solution $x \in \mathbb{R}^n$ that solves the optimization problem:

$$\underset{x \in \mathbb{R}^n}{\arg\min} \frac{1}{2} ||Ax - b||_2^2 + \lambda ||x||_1$$

This problem can be solved with many iterative algorithms. We consider the accelerated BCD (accBCD) algorithm described in [11, Algorithm 2] (see Algorithm 1 in this paper). Nesterov's acceleration [33] is adapted to BCD [11] through the introduction of additional vectors $(y_h, z_h, \tilde{y}_h, \text{ and } \tilde{z}_h)$ and scalar (θ_h) updates. The solution vector at each iteration h is implicitly computed since $x_h = \theta_h y_h + z_h$, but need not be explicitly computed until termination. Aside from complications due to acceleration, the remaining computations follow the BCD steps illustrated in Figure 1.

We assume that A is 1D-row partitioned so partial dotproducts (lines 8 and 9 in Alg. 1) can be computed on each processor. Lines 4-7 in Alg. 1 randomly select μ column indices and extract them from A. Lines 8-9 in Alg. 1 compute dot-products. Lines 11-17 Alg. 1 compute the solution to the subproblem and update scalars and vectors for the next iteration. We assume that H iterations of the algorithm are performed (where H depends on the termination criterion). Line 12 in Alg. 1 applies the softthresholding function defined in (2) required for Lasso. At each iteration we compute v, the optimal Lipschitz constant, which is the largest eigenvalue of the, small, $\mu \times \mu$ Gram matrix (line 10 in Alg 1). Since G is replicated on all processors (after the MPI_Allreduce [21]), line 10 does not require communication. An approximate Lipschitz constant can be used but we compute the optimal constant for fast convergence. The recurrences in lines 9-17 of Alg. 1 can be unrolled to avoid synchronization. We begin the SA derivation by changing the loop index from h to sk + jwhere k is the outer loop index, s is the recurrence unrolling parameter, and j is the inner loop index. Let us assume that we are at iteration sk + 1 and have just computed the vectors z_{sk} , \tilde{z}_{sk} , y_{sk} , and \tilde{y}_{sk} . From this Δz_{sk+1} can be computed by¹

Algorithm 2 Synchronization-Avoiding Accelerated Block Coordinate Descent (SA-accBCD) for Lasso

1: Input:
$$A \in \mathbb{R}^{m \times n}, b \in \mathbb{R}^{m}, H > 1, y_{0} \in \mathbb{R}^{n}, z_{0} \in \mathbb{R}^{n}$$

 $\lambda \in \mathbb{R}, \mu \in \mathbb{Z}_{+} \text{ s.t. } \mu \leq n$
2: $\theta_{0} = \mu/n, \tilde{y}_{0} = Ay_{0}, \tilde{z}_{0} = Az_{0} - b$
3: $q = \lceil n/\mu \rceil$
4: for $k = 1, 2, \dots, \frac{H}{s}$ do
5: for $j = 1, 2, \dots, s$ do
6: choose $\{i_{l} \in [n]|l = 1, 2, \dots, \mu\}$ uniformly at random without replacement.
7: $\mathbb{I}_{sk+j} = [e_{i_{1}}, e_{i_{2}}, \dots, e_{i_{\mu}}]$
8: Let $\mathbb{A}_{sk+j} = A\mathbb{I}_{sk+j}$
9: $\theta_{sk+j} = \sqrt{\frac{\theta_{sk+j-1}^{4} + \theta_{sk+j-1}^{2}}{2}}$
Communication: Lines 11 and 12.
10: Let $Y = [\mathbb{A}_{sk+1}, \mathbb{A}_{sk+2}, \dots, \mathbb{A}_{sk+s}]$.
11: $G = Y^{T}Y$.
12: $\begin{bmatrix} \tilde{y}'_{sk+1} & \tilde{y}'_{sk+2} & \dots & \tilde{y}'_{sk+s} \\ \tilde{z}'_{sk+1} & \tilde{z}'_{sk+2} & \dots & \tilde{z}'_{sk+s} \end{bmatrix}^{T} = Y^{T} [\tilde{y}_{sk} \quad \tilde{z}_{sk}]$.
13: for $j = 1, 2, \dots, s$ do
14: $v = \text{large eigenvalue of } \mathbb{A}^{T}_{sk+j}\mathbb{A}_{sk+j}$.
15: $\eta_{sk+j} = \frac{1}{q\theta_{sk+j-1}e_{sk+j}^{T}v}$
16: Compute r_{sk+j} by equation²(3)
17: Compute g_{sk+j} by equation (4)
18: Compute Δz_{sk+j} by equation (5)
19: $z_{sk+j} = \tilde{z}_{sk+j-1} + \mathbb{I}_{sk+j}\Delta z_{sk+j}$.
19: $z_{sk+j} = \tilde{z}_{sk+j-1} + \mathbb{I}_{sk+j}\Delta z_{sk+j}$.
20: $\tilde{z}_{sk+j} = \tilde{y}_{sk+j-1} - \frac{1-q\theta_{sk+j-1}}{\theta_{sk+j-1}^{2}}\mathbb{I}_{sk+j}\Delta z_{sk+j}$.
21: $y_{sk+j} = \tilde{y}_{sk+j-1} - \frac{1-q\theta_{sk+j-1}}{\theta_{sk+j-1}^{2}}\mathbb{I}_{sk+j}\Delta z_{sk+j}$.
22: $\tilde{y}_{sk+j} = \tilde{y}_{sk+j-1} + Z_{sk+j}$.

$$\begin{aligned} r_{sk+1} &= \mathbb{A}_{sk+1}^{T} \left(\theta_{sk}^{2} \tilde{y}_{sk} + \tilde{z}_{sk} \right), \\ g_{sk+1} &= \mathbb{I}_{sk+1}^{T} z_{sk} - \eta_{sk+1} r_{sk+1}, \\ \Delta z_{sk+1} &= S_{\lambda \eta_{sk+1}} (g_{sk+1}) - \mathbb{I}_{sk+1}^{T} z_{sk}. \end{aligned}$$

By unrolling the vector update recurrences for z_{sk+1} , \tilde{y}_{sk+1} , and \tilde{z}_{sk+1} (lines 14, 15, and 17), we can compute r_{sk+2} , g_{sk+2} , and Δz_{sk+2} in terms of z_{sk} , \tilde{z}_{sk} , and \tilde{y}_{sk}

$$\begin{aligned} r_{sk+2} &= \mathbb{A}_{sk+2}^{T} \left(\theta_{sk+1}^{2} \tilde{y}_{sk} - \theta_{sk+1}^{2} \frac{1 - q\theta_{sk}}{\theta_{sk}^{2}} \mathbb{A}_{sk+1} \Delta z_{sk+1} \right) \\ &+ \tilde{z}_{sk} + \mathbb{A}_{sk+1} \Delta z_{sk+1} \right), \\ &= \theta_{sk+1}^{2} \mathbb{A}_{sk+2}^{T} \tilde{y}_{sk} + \mathbb{A}_{sk+2}^{T} \tilde{z}_{sk} \\ &- \left(\theta_{sk+1}^{2} \frac{1 - q\theta_{sk}}{\theta_{sk}^{2}} - 1 \right) \mathbb{A}_{sk+2}^{T} \mathbb{A}_{sk+1} \Delta z_{sk+1}, \\ g_{sk+2} &= \mathbb{I}_{sk+2}^{T} z_{sk} + \mathbb{I}_{sk+2}^{T} \mathbb{I}_{sk+1} \Delta z_{sk+1} - \eta_{sk+2} r_{sk+2}, \\ \Delta z_{sk+2} &= S_{\lambda \eta_{sk+2}} (g_{sk+2}) - \mathbb{I}_{sk+2}^{T} z_{sk} - \mathbb{I}_{sk+2}^{T} \mathbb{I}_{sk+1} \Delta z_{sk+1}. \end{aligned}$$

¹We ignore scalar updates since they can be redundantly stored and computed on all processors.

By induction we can show that r_{sk+j} , g_{sk+j} , and Δz_{sk+j} can be computed in terms of z_{sk} , \tilde{z}_{sk} , and \tilde{y}_{sk}

$$r_{sk+j} = \theta_{sk+j-1}^{2} \mathbb{A}_{sk+j}^{T} \tilde{y}_{sk} + \mathbb{A}_{sk+j}^{T} \tilde{z}_{sk} - \sum_{t=1}^{j-1} \left(\theta_{sk+j-1}^{2} \frac{1 - q\theta_{sk+t-1}}{\theta_{sk+t-1}^{2}} - 1 \right) \mathbb{A}_{sk+j}^{T} \mathbb{A}_{sk+t} \Delta z_{sk+t}$$
(3)

$$g_{sk+j} = \mathbb{I}_{sk+j}^T z_{sk} - \eta_{sk+j} r_{sk+j} + \sum_{t=1}^{j-1} \mathbb{I}_{sk+j}^T \mathbb{I}_{sk+t} \Delta z_{sk+t}$$

$$\tag{4}$$

$$\Delta z_{sk+j} = S_{\lambda \eta_{sk+j}}(g_{sk+j}) - \mathbb{I}_{sk+j}^T z_{sk} - \sum_{t=1}^{j-1} \mathbb{I}_{sk+j}^T \mathbb{I}_{sk+t} \Delta z_{sk+t}$$
(5)

for j = 1, 2, ..., s. Notice that due to the recurrence unrolling we can defer the updates to z_{sk} , y_{sk} , \tilde{z}_{sk} , and \tilde{y}_{sk} for s iterations. The summation in (3) computes the Gram-like matrices, $\mathbb{A}_{sk+j}^T \mathbb{A}_{sk+t}$, for $t = 1, 2, \cdots, j - 1$. Synchronization can be avoided in these computations by computing the $s\mu \times s\mu$ Gram matrix $G = [\mathbb{A}_{sk+1}, \mathbb{A}_{sk+2}, \cdots, \mathbb{A}_{sk+s}]^T [\mathbb{A}_{sk+1}, \mathbb{A}_{sk+2}, \cdots, \mathbb{A}_{sk+s}]$ once before the inner loop³ and redundantly storing it on all processors. Synchronization can be avoided in the summation in (4) by initializing the random number generator on all processors to the same seed. Finally, at the end of the s inner loop iterations we can perform the vector updates

$$z_{sk+s} = z_{sk} + \sum_{t=1}^{s} \mathbb{I}_{i_{sk+t}} \Delta z_{sk+t}$$

$$\tag{6}$$

$$\tilde{z}_{sk+s} = \tilde{z}_{sk} + \sum_{t=1}^{s} \mathbb{A}_{sk+t} \Delta z_{sk+t}$$
(7)

$$y_{sk+s} = y_{sk} - \sum_{t=1}^{s} \frac{1 - q\theta_{sk+t-1}}{\theta_{sk+t-1}^2} \mathbb{I}_{sk+t} \Delta z_{sk+t}$$
(8)

$$\tilde{y}_{sk+s} = \tilde{y}_{sk} - \sum_{t=1}^{s} \frac{1 - q\theta_{sk+t-1}}{\theta_{sk+t-1}^2} \mathbb{A}_{sk+t} \Delta z_{sk+t}.$$
 (9)

The resulting Synchronization-Avoiding accBCD (SAaccBCD) algorithm is shown in Algorithm 2. Since our SA technique relies on rearranging the computations, the convergence rates and behavior of the standard accelerated BCD algorithm (Alg. 1) is the same (in exact arithmetic).

SA-accBCD computes a larger $s\mu \times s\mu$ Gram matrix every s iterations, which results in a computation-communication tradeoff where SA-accBCD increases the flops and message size in order to reduce the latency by s. If the latency cost is the dominant term then SA-accBCD can attain s-fold

²Since $\mathbb{A}_{sk+j}^T \tilde{y}_{sk} = \tilde{y}'_{sk}$ and $\mathbb{A}_{sk+j}^T \tilde{y}_{sk} = \tilde{z}'_{sk}$, no additional computation or communication is needed to form those vectors.

 3G is symmetric so computing just the upper/lower triangular part reduces flops and message size by 2×.

Algorithm 3 Dual Coordinate Descent for Linear SVM (SVM)

1: Input: $A \in \mathbb{R}^{m \times n}, b \in \mathbb{R}^m, H > 1, \lambda \in \mathbb{R}, \alpha_0 \in \mathbb{R}^m$ 2: $x_0 = \sum_{j=1}^m y_i \alpha_i A_i^T$ 3: for $h = 1 \dots H$ do $i_h \in [m]$, chosen uniformly at random. 4: $\mathbb{I}_h = [e_{i_h}]$ 5: Let $\mathbb{A}_h = \mathbb{I}_h^T A$ 6: Communication: Lines 7 and 8. 7. $\eta_h = \mathbb{A}_h \mathbb{A}_h^T + \gamma$
$$\begin{split} & \widehat{g}_h = \mathbb{I}_h^T b \mathbb{A}_h x_{h-1} - 1 + \gamma \mathbb{I}_h^T \alpha_{h-1} \\ & \widehat{g}_h = |\min(\max(\mathbb{I}_h^T \alpha_{h-1} - g_h, 0), \nu) - \mathbb{I}_h^T \alpha_{h-1}| \end{split}$$
8: 9: if $\tilde{g}_h \neq 0$ then 10: $\theta_h = \min(\max(\mathbb{I}_h^T \alpha_{h-1} - \frac{g_h}{\eta_h}, 0), \nu) - \mathbb{I}_h^T \alpha_{h-1}$ 11: 12: else 13: $\theta_h = 0$ $\begin{aligned} \alpha_h &= \alpha_{h-1} + \theta_h \mathbb{I}_h \\ x_h &= x_{h-1} + \theta_h \mathbb{I}_h^T b \mathbb{A}_h^T \end{aligned}$ 14: 15: 16: Output: x_H

speedup over accBCD. In general there exists a tradeoff between *s* and the speedups attainable. Table I summarizes the operational, storage, and communication costs of the SA and non-SA methods.

IV. SYNCHRONIZATION-AVOIDING SVM

We are given a matrix $A \in \mathbb{R}^{m \times n}$, labels $b \in \mathbb{R}^m$ where b_i are binary labels $\{-1, +1\}$ for each observation A_i (*i*-th row of A). Support Vector Machines (SVM) solve the optimization problem:

$$\underset{x \in \mathbb{R}^n}{\arg\min} \frac{1}{2} ||x||_2^2 + \lambda \sum_{i=1}^m F(A_i, b_i, x)$$
(10)

wher $F(A_i, b_i, x)$ is a loss function and $\lambda > 0$ is the penalty parameter. In this work, we consider the two loss functions:

$$\max(1 - b_i A_i x, 0)$$
 and $\max(1 - b_i A_i x, 0)^2$. (11)

We refer to the first as SVM-L1 and the second as SVM-L2 (consistent with [19]). Recent work [19] has shown that both variants of SVM can be solved efficiently using dual coordinate descent. Therefore, in this work we will consider the dual optimization problem:

$$\underset{\alpha \in \mathbb{R}^m}{\arg\min} \frac{1}{2} \alpha^T \bar{Q} \alpha - e^T \alpha \tag{12}$$

subject to
$$0 \le \alpha_i \le \nu, \forall i,$$
 (13)

where $\bar{Q} = Q + D$, where $D = \gamma I_m$ and $Q_{ij} = b_i b_j A_i A_j^T$. For SVM-L1, $\gamma = 0$ and $\nu = \lambda$ and for SVM-L2 $\gamma = \frac{.5}{\lambda}$ and $\nu = \infty$. The dual problem can be solved efficiently by CD [19] and is shown in Algorithm 3. Note that, unlike Lasso, SVM requires 1D-column partitioning in order to compute dot-products in parallel. The recurrences defined

Table I: Ops (F), Latency (L), Bandwidth (W) and Memory per processor (M) costs comparison along the critical path of classical accBCD and SA-accBCD. *H* is the number of iterations and we assume that $A \in \mathbb{R}^{m \times n}$ is sparse with fmn non-zeros that are uniformly distributed, $0 < f \leq 1$ is the density of *A* (i.e. $f = \frac{nnz(A)}{mn}$), *P* is the number of processors and *s* is the recurrence unrolling parameter. $f\mu m$ is the non-zeros of the $\mu \times m$ matrix with μ sampled columns from *A* at each iteration. We assume that the $\mu \times \mu$ and Gram matrix computed at each iteration are dense.

Summary of theoretical costs				
Algorithm	Ops cost (F)	Memory cost (M)	Latency cost (L)	Message Size cost (W)
accBCD	$O\left(\frac{H\mu^2 fm}{P} + H\mu^3\right)$	$O\left(\frac{fmn+m}{P} + \mu^2 + n\right)$	$O\left(H\log P\right)$	$O\left(H\mu^2\log P\right)$
SA-accBCD	$O\left(\frac{H\mu^2 sfm}{P} + H\mu^3\right)$	$O\left(\frac{fmn+m}{P} + \mu^2 s^2 + n\right)$	$O\left(\frac{H}{s}\log P\right)$	$O\left(H\boldsymbol{s}\mu^2\log P\right)$

in lines 7-11, 13, and 14 of Alg. 3 can be unrolled to avoid synchornization. We begin the SA derivation by changing the loop index from h to sk + j where k is the outer loop index, s is the (tunable) recurrence unrolling parameter, and j is the inner loop index. Let us assume that we are at iteration sk + 1 and have just computed the vectors x_{sk} and α_{sk} . From this the next update, θ_{sk+1} , can be computed by

$$\begin{split} g_{sk+1} &= \mathbb{I}_{sk+1}^{T} b \mathbb{A}_{sk+1} x_{sk} - 1 + \gamma \mathbb{I}_{sk+1}^{T} \alpha_{sk}, \\ \tilde{g}_{sk+1} &= |\min(\max(\mathbb{I}_{sk+1}^{T} \alpha_{sk} - g_{sk+1}, 0), \nu) - \mathbb{I}_{sk+1}^{T} \alpha_{sk}|, \\ \theta_{sk+1} &= \begin{cases} \min(\max(\mathbb{I}_{sk+1}^{T} \alpha_{sk} - \frac{g_{sk+1}}{\eta_{sk+1}}, 0), \nu) - \mathbb{I}_{sk+1}^{T} \alpha_{sk}, \\ & \text{when } \tilde{g}_{sk+1} \neq 0 \\ 0, & \text{otherwise.} \end{cases} \end{split}$$

Finally, α_{sk+1} and x_{sk+1} can be computed by

$$\begin{aligned} \alpha_{sk+1} &= \alpha_{sk} + \theta_{sk+1} \mathbb{I}_{sk+1}, \\ x_{sk+1} &= x_{sk} + \theta_{sk+1} \mathbb{I}_{sk+1}^T b \mathbb{A}_{sk+1}^T \end{aligned}$$

By unrolling the vector update recurrences for α_{sk+1} and x_{sk+1} , we can compute g_{sk+2} , \tilde{g}_{sk+2} , and θ_{sk+2} in terms of α_{sk} and x_{sk} . We will ignore the quantities \tilde{g}_{sk+j} and θ_{sk+j} in the subsequent derivations for brevity. We introduce an auxiliary scalar, $\beta_{sk+j} = \mathbb{I}_{sk+j}^T \alpha_{sk+j-1}$, for notational convenience.

$$\beta_{sk+2} = \mathbb{I}_{sk+2}^T \alpha_{sk} + \theta_{sk+1} \mathbb{I}_{sk+2}^T \mathbb{I}_{sk+1},$$

$$g_{sk+2} = \mathbb{I}_{sk+2}^T b \mathbb{A}_{sk+2} x_{sk} - 1 + \gamma \beta_{sk+2}$$

$$+ \theta_{sk+1} \mathbb{I}_{sk+2}^T b \mathbb{I}_{sk+1}^T b \mathbb{A}_{sk+2} \mathbb{A}_{sk+1}^T.$$

By induction we can show that g_{sk+j} can be computed in terms of α_{sk} and x_{sk} such that

$$\beta_{sk+j} = \mathbb{I}_{sk+j}^T \alpha_{sk} + \sum_{t=1}^{j-1} \theta_{sk+t} \mathbb{I}_{sk+j}^T \mathbb{I}_{sk+t}, \qquad (14)$$

$$g_{sk+j} = \mathbb{I}_{sk+j}^T b \mathbb{A}_{sk+j} x_{sk} - 1 + \gamma \beta_{sk+j} + \sum_{t=1}^{j-1} \theta_{sk+t} \mathbb{I}_{sk+j}^T b \mathbb{I}_{sk+t}^T b \mathbb{A}_{sk+j} \mathbb{A}_{sk+t}^T,$$
(15)

Algorithm 4 Synchronization-Avoiding Dual Coordinate Descent for Linear SVM (SA-SVM)

1:	Input: $A \in \mathbb{R}^{m \times n}, b \in \mathbb{R}^m, H > 1, \lambda \in \mathbb{R}, s \in \mathbb{Z}^+$
	$\alpha_0 \in \mathbb{R}^m$,
	$\gamma = \begin{cases} 0, \text{ for SVM-L1} \\ 0, 0 \end{cases}$ $\nu = \begin{cases} \lambda, \text{ for SVM-L1} \\ \lambda \end{cases}$
	$\left(\frac{.5}{\lambda}, \text{ for SVM-L2}\right)^{\prime} \left(\infty, \text{ for SVM-L2}\right)$
2:	$x_0 = \sum_{i=1}^m y_i \alpha_i A_i^T$
3:	for $k = 0, \ldots, \frac{H}{s}$ do
4:	for $j = 1 \dots s$ do
5:	$i_{sk+j} \in [m]$, chosen uniformly at random.
6:	Let $\mathbb{I}_{sk+j} = \begin{bmatrix} e_{i_{sk+j}} \end{bmatrix}$
7:	Let $\mathbb{A}_{sk+j} = \mathbb{I}_{sk+j}^T A$
8:	Let $Y = \begin{bmatrix} \mathbb{A}_{sk+1}^T, \mathbb{A}_{sk+2}^T, \dots, \mathbb{A}_{sk+j}^T \end{bmatrix}$.
	Communication: Lines 9 and 10.
9:	$G = Y^T Y + \gamma I_s.$
10:	$[x'_{sk+1},\ldots,x'_{sk+s}]^T = Y^T x_{sk}$
11:	$[\eta_{sk+1},\ldots,\eta_{sk+s}]^T = \operatorname{diag}(G)$
12:	for $j=1,\ldots,s$ do
13:	Compute β_{sk+j} according to equation (14).
14:	Compute g_{sk+j} according to equation ⁴ (15).
15:	$\tilde{g}_h = \min(\max(\beta_{sk+j} - g_{sk+j}, 0), \nu) - \beta_{sk+j} $
16:	if $\tilde{g}_h \neq 0$ then
17:	$\theta_{sk+j} = \min(\max(\beta_{sk+j} - \frac{g_{sk+j}}{\eta_{sk+j}}, 0), \nu)$
	$-\beta_{sk+j}$
18:	else
19:	$\theta_{sk+j} = 0$
20:	$\alpha_{sk+j} = \alpha_{sk+j-1} + \theta_{sk+j} \mathbb{I}_{sk+j}$
21:	$x_{sk+j} = x_{sk+j-1} + \theta_{sk+j} \mathbb{I}_{sk+j}^T b \mathbb{A}_{sk+j}^T$
22:	Output: x _H

for j = 1, 2, ..., s. Due to the recurrence unrolling, we can defer updates to α_{sk} and x_{sk} for s iterations. The summation in (14) adds a previous update θ_{sk+t} if the coordinate chosen for update at iteration sk+t is the same as iteration sk+j. Synchronization can be avoided in the steps of this algorithm in much the same way as SA-accBCD for

⁴Since $\mathbb{A}_{sk+j}x_{sk+j} = x'_{sk+j}$, no additional computation or communication is needed to form the vector.

Table II: Properties of the LIBSVM datasets used in Lasso numerical and performance experiments.



Figure 2: We compare the convergence of accCD, CD, accBCD, BCD against their SA variants (with s = 1000). $\lambda = 100\sigma_{min}$, where σ_{min} is the smallest singular value. $\mu = 1$ for accCD and CD. $\mu = 8$ for accBCD and BCD.

Lasso. The resulting SA-SVM algorithm is shown in Alg. 4. The derivation we present in this section only rearranges the algebra. Hence, the convergence rates and behavior of SVM (Alg. 3) do not change (in exact arithmetic).

V. EXPERIMENTAL RESULTS

In this section we present experimental results for our SA variant and explore the numerical and performance tradeoffs. The recurrence unrolling we propose requires computation of Gram-like matrices whose condition numbers may adversely affect the numerical stability of SA-accBCD and SA-SVM. We also re-order the sequence of updates of the solution and residual vectors, which could also lead to numerical instability. We begin in Section V-A with experiments that illustrate that our SA-variants are numerically stable.

A. Convergence behavior

We explore the tradeoff between convergence behavior, block size, and s (the recurrence unrolling parameter) for the SA-accBCD algorithm and compare it to the behavior of the standard accBCD algorithm. All numerical stability Table III: Final relative objective error of the SA vs. non-SA methods (from Figure 2). Machine precision is 2.2e-16.

Relative objective error				
	url covtype ne			
SA-accCD	2.2176e-16	2.1514e-16	6.6324e-17	
SA-CD	2.2204e-16	1.4203e-16	3.2567e-17	
SA-accBCD	2.2204e-16	2.2616e-16	5.6153e-17	
SA-BCD	2.2204e-16	2.6451e-16	8.8625e-17	

Table IV: Properties of the LIBSVM datasets used in the SVM numerical and performance experiments.

Summary of datasets			
Name	m (Data Points)	n (Features)	NNZ%
news20.binary	1,355,191	19,996	0.03
rcv1.binary	47,236	20,242	0.16
gisette	5,000	6,000	99



(c) gisette

Figure 3: Duality gap vs. iterations of SVM-L1, and its SA variants with s = 500.

experiments were performed in C++ on a Cray XC30 platform using MPI for parallel processing. The datasets used in our experiments were obtained from the LIBSVM repository [22] and are summarized in Table II. The datasets were chosen such that we tested our methods against a variety sparse/dense, small/large, and well/ill-conditioned problems. We measure the convergence behavior by plotting the objective function value: $f(A, b, x_h) = \frac{1}{2} ||Ax_h - b||_2^2 + \lambda ||x_h||_1$ at each iteration. For all experiments, we set $\lambda = 100\sigma_{min}$. We report the convergence vs. iterations to illustrate any differences in convergence behavior.

Figure 2 shows the convergence behavior of the datasets



(a) news20; Processors = 768 (b) url; Processors = 12288 (c) covtype; Processors = 3072 (d) epsilon; Processors = 12288 Figure 4: We compare SA vs non-SA running times vs. convergence of CD, accCD (top row), BCD, accBCD (bottom row).

in Table II for several blocksizes and with s = 1000 for SA-accBCD. The results show that larger blocksizes converge faster than $\mu = 1$, but at the expense of more computation (and larger message sizes in the distributed-memory setting). Comparing SA-accBCD and accBCD we observe no numerical stability issues for *s* as large as 1000 for all datasets tested (in theory we can avoid communication for 1000 iterations). Table III shows the final relative objective error of the SA-methods compared to the non-SA methods: $\frac{|f(.)_{non-SA} - f(.)_{SA}|}{f(.)_{non-SA}}$. This suggests that the additional computation and message size costs are the performance limiting factors and not numerical instability.

We conduct similar numerical stability experiments for SA-SVM and use binary classification datasets (summarized in Table IV) from the LIBSVM [22] repository. We measure the convergence behavior by plotting the duality gap, $P(x) - D(\alpha)$, where P(x) is the primal objective value⁵ and $D(\alpha)$ is the dual objective value (as in [19]). Note that duality gap is a stronger criterion than the relative objective error used in Sec. V-B. Due to strong convexity, primal and dual linear SVM have the same optimal function value [15], [19]. We set $\lambda = 1$ for all experiments (same as [19]) and show results for the harder (non-smooth), SVM-L1 problem.

Figure 3 illustrates that the SA-variants with s = 500 are numerically stable and converge in the same way their non-SA variants. Similar to the Lasso stability experiments we found that the relative error in the duality gap was close to machine precision. These experiments illustrate the numerical stability of our approach. Next, we explore the performance trade-offs of the SA methods.

B. Performance results

In this section, we present experimental results to show that the SA-methods in Section V-A are faster than their non-SA variants. We consider the datasets in Table II which were chosen to illustrate performance and speedups on over/under-determined, sparse and dense datasets to illustrate that speedups are independent of those factors.

We implement the algorithms in C++ using the Message Passing Interface (MPI) [21] for high-performance, distributed-memory parallel processing. The local linear algebra computations are performed using the Intel MKL library for Sparse and Dense BLAS [34] routines. All methods were tested on a Cray XC30 supercomputer at NERSC which has 24 processors per node and 128GB of memory. The implementation divides the dataset row-wise, however, the SA-methods generalize to other data layout schemes. We choose row-wise since it results in the lowest per iteration communication cost of $O(\log P)$ [27]. All datasets are stored using Compressed Sparse Row format (3array variant). Vectors in \mathbb{R}^n are replicated on all processors and vectors in \mathbb{R}^m are partitioned (similar to A).

Convergence vs. Running Time: Figure 4 shows the convergence vs. running time for the datasets in Table II. We present experiments on CD, accCD, BCD, accBCD and their SA variants. In all plots, we can observe that the accelerated methods converge faster than the non-accelerated methods. The BCD methods converge faster than the CD methods as expected. Since the SA-methods do not alter the convergence rates they are faster per iteration.

For the SA methods, we plot two values of s, one value (in blue) where we observed the best speedups and a larger value (in red) where we observed less speedups.

 $^{^{5}}$ We can compute this without running the primal SVM algorithm (as in [19]).

Table V: Strong scaling performance of SA and non-SA variants of accCD.

Running Times (s)				
	news20	url	covtype	epsilon
accCD	P = 192, 62.9s	P = 3072, 172.7s	P = 768, 1.5s	P = 3072, 6.3s
	P = 384, 51.6s	P = 6144, 162.1s	P = 1536, 0.9s	P = 6144, 3.7s
	P = 768, 43.5s	P = 12288, 108.5s	P = 3072, 1.07s	P = 12288, 2.97s
SA-accCD	P = 192, 36.7s, s = 32	P = 3072, 110.3s, s = 256	P = 768, 0.328s, s = 16	P = 3072, 3.58s, s = 32
	P = 384, 20.8s, s = 32	P = 6144, 69.2s, s = 256	P = 1536, 0.234s, s = 32	P = 6144, 2.0s, s = 32
	P = 768, 15.7s, s = 16	P = 12288, 40.6s, s = 128	P = 3072, 0.210s, s = 32	P = 12288, 1.08s, s = 64

Table VI: SA-SVM-L1 speedups over SVM-L1. s = 64 was the best setting for rcv1 and news20 datasets; s = 128 was best for gisette. We use a duality gap tolerance of 1e-1.

SA-SVM Speedups				
Dataset	Processors	Algorithm	Running Time (speedup)	
news20.binary	P = 576	SVM-L1	258 sec.	
		SA-SVM-L1	121 sec. $(2.1 \times)$	
rcv1.binary	P = 240	SVM-L1	208 sec.	
		SA-SVM-L1	149 sec. $(1.4 \times)$	
gisette	P = 3072	SVM-L1	230 sec.	
		SA-SVM-L1	57 sec. $(4\times)$	

Note that this decrease in speedup for certain values of *s* is expected since the SA-methods tradeoff additional message size and computation for a decrease in latency cost. We see SA-accCD speedups of $2.8 \times , 5.1 \times , 2.8 \times$, and $2.7 \times$ for news20, url, covtype, and epsilon, respectively. For SA-accBCD the speedups decrease to $1.2 \times , 2.1 \times , 4.4 \times$, and $1.8 \times$, respectively.

Performance Scaling and Speedups: Table V shows the performance *strong scaling* (problem size is fixed and number of processors is increased) of the accCD vs. SAaccCD methods for different ranges of processors for the datasets tested. We can observe that SA-accCD is faster for all datasets and for all processor ranges. Notice that the running time gap between accCD and SA-accCD increases with the number of processors.

Table VI shows speedups of SA-SVM-L1 over SVM-L1. SVM-L1 and SVM-L2 are initialized with different scalar quantities. All else remains the same, so we solve the (harder) SVM-L1 problem and report performance results. Note that we perform offline strong scaling experiments for each dataset and report the best processors and running time combinations. We observed speedups of $1.4 \times$ for rcv1.binary, $2.1 \times$ for news20.binary and $4 \times$ for gisette. These speedups were attained despite load balancing issues for rcv1.binary and news20.binary when transforming datasets stored row-wise on disk to 1D-column partitioned matrices in DRAM (Lasso experiments do not have these issues). Eliminating this overhead in future work would further improve speedups and scalability (since load imbalance decreases the effective flops rate due to stragglers). Gisette is nearly dense hence load balance was not a problem. We were able to strong scale SA-SVM-L1 to 3072 cores and attain a $4\times$ speedup.

VI. CONCLUSION

We showed in this paper that existing work in CA-NLA can be extended to important ML problems. We derived SA variants of BCD methods and illustrated that our methods are faster and more scalable without altering convergence rates. The SA-variants attain running time speedups of $1.2 \times -5.1 \times$ over the standard algorithms. While we did not explore other parallel environments, our methods would attain greater speedups on frameworks like Spark [35] due to the large latency costs [36].

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